Application Note

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Application Note

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In order to simulate AT power behaviour using an ATX power supply, +5VSB (standby) must be connected to Pin 9 of the Front Panel Connector (J1). Refer to Figure 2-2. Jumper and Connector Locations (p. 2-4) for the connector location and Table 2-10. Front Panel Connector (p. 2-8) for the pin definition in the PCI-755-VG2 User's Manual.

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