Errata 1

Manual: 24208

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1. Reference: Page 1 - 7, Chapter 1.5, Table 1-3

The referenced information (table) is revised as follows.

The table row:

CompactPCI Interface ***(See note at foot of table)***	Realized on 132-pin, female, six row connector (standard CPCI type connector for J1)	Bus Width: 32-bit, Bus Speed: 33 MHz 64 kB memory space, non-prefetchable PCI Header: Device ID: 0x5555 Vendor ID: 0x1556 Class Code: 0x110000 Subsystem Device ID: 0x00F0 Subsystem Vendor ID: 0x1518

is changed to read:

CompactPCI Interface ***(See note at foot of table)***	Realized on 132-pin, female, six row connector (standard CPCI type connector for J1)	Bus Width: 32-bit, Bus Speed: 33 MHz 64 kB memory space, non-prefetchable PCI Header: Device ID: 0x5555 Vendor ID: 0x1556 Class Code: 0x110000 Subsystem Device ID: 0x00F0 Subsystem Vendor ID: 0x1518
	PCI Interface	Universal; 3.3V (5V-tolerant); the VI/O pins are not connected

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