Errata 1

Manual: 31440

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1. Reference: Page 1 - 10, Chapter 1.6, Table 1-3

The referenced information (table) is revised as follows.

The table row:

PCI Interface	VI/O voltage is neither relevant nor used

is changed to read:

PCI Interface	Universal; 3.3V (5V-tolerant); the VI/O pins are not connected

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