Errata 1

Manual: 22241

Manual Index: 02

Table of Contents:

1. Reference: Page 2 - 32, Chapter 2.7.5 ..... 1 - 3

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1. Reference: Page 2 - 32, Chapter 2.7.5

The referenced information (entire chapter) is superceded by the following replacement chapter.

2.7.5 PCI V(I/O) Setting

The CP302 has an universal CompactPCI interface (3.3V/5V) in accordance with the CompactPCI Specification PICMG 2.0 Rev. 2.1. The CompactPCI V(I/O) must be configured via the backplane.

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