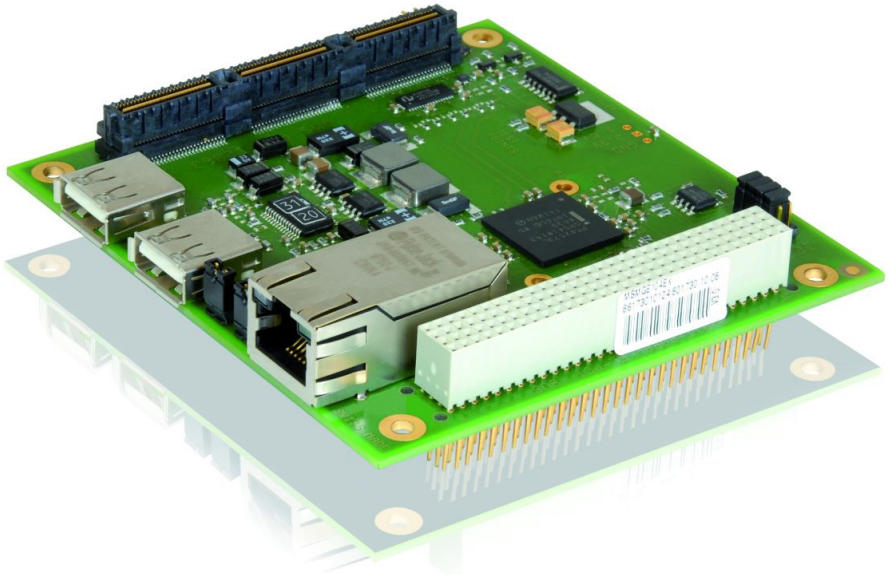
» Kontron User's Guide «



MSMGE104EX

Document Revision 101

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1 User Information

1.1 About this Document

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For the circuits, descriptions and tables indicated, Kontron assumes no responsibility as far as patents or other rights of third parties are concerned.

1.2 Copyright Notice

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1.3 Trademarks

The following lists the trademarks of components used in this board.

- IBM, XT, AT, PS/2 and Personal System/2 are trademarks of International Business Machines Corp.
- Microsoft is a registered trademark of Microsoft Corp.
- Intel is a registered trademark of Intel Corp.
- All other products and trademarks mentioned in this manual are trademarks of their respective owners.

1.4 Standards

Kontron AG is certified to ISO 9000 standards.

1.5 Warranty

This Kontron AG product is warranted against defects in material and workmanship for the warranty period from the date of shipment. During the warranty period, Kontron AG will, at its discretion, decide to repair or replace defective products.

Within the warranty period, the repair of products is free of charge as long as warranty conditions are observed.

The warranty does not apply to defects resulting from improper or inadequate maintenance or handling by the buyer, unauthorized modification or misuse, operation outside of the product's environmental specifications or improper installation or maintenance.

Kontron AG will not be responsible for any defects or damages to other products not supplied by Kontron AG that are caused by a faulty Kontron AG product.

1.6 Technical Support

Technicians and engineers from Kontron AG and/or its subsidiaries are available for technical support. We are committed to making our products easy to use and will help you use our products in your systems.

Please consult our Web site at http://www.kontron.com/support for the latest product documentation, utilities, drivers and support contacts. Consult our customer section http://emdcustomersection.kontron.com for the latest BIOS downloads, Product Change Notifications and additional tools and software. In any case, you can always contact your board supplier for technical support.

1.7 Environmental Protection Statement

This product has been manufactured to satisfy environmental protection requirements wherever possible. Many of the components used (structural parts, printed circuit boards, connectors, batteries, etc.) are capable of being recycled. Final disposal of this product after its service life must be accomplished in accordance with applicable country, state, or local laws or regulations. All components within this product fulfill the requirements of the RoHS (Restriction of Hazardous Substances Directive). The product is soldered with a lead free process.

1.8 RoHS Commitment

Kontron Compact Computers AG (Switzerland) is committed to develop and produce environmentally friendly products according to the Restriction of Hazardous Substances (RoHS) Directive (2002/95/EC) and the Waste Electrical and Electronic Equipment (WEEE) Directive (2002/96/EC) established by the European Union. The RoHS directive was adopted in February 2003 by the European Union and came into effect on July 1, 2006. It is not a law but a directive, which restricts the use of six hazardous materials in the manufacturing of various types of electronic and electrical equipment. It is closely linked with the Waste Electrical and Electronic Equipment Directive (WEEE) 2002/96/EC, which has set targets for collection, recycling and recovery of electrical goods and is part of a legislative initiative to solve the problem of huge amounts of toxic e-waste.

Each European Union member state is adopting its own enforcement and implementation policies using the directive as a guide. Therefore, there could be as many different versions of the law as there are states in the EU. Additionally, non-EU countries like China, Japan, or states in the U.S. such as California may have their own regulations for green products, which are similar, but not identical, to the RoHS directive.

RoHS is often referred to as the "lead-free" directive but it restricts the use of the following substances:

- Lead
- Mercury
- Cadmium
- Chromium VI
- PBB and PBDE

The maximum allowable concentration of any of the above mentioned substances is 0.1% (except for Cadmium, which is limited to 0.01%) by weight of homogeneous material. This means that the limits do not apply to the weight of the finished product, or even to a component but to any single substance that could (theoretically) be separated mechanically.

1.8.1 RoHS Compatible Product Design

All Kontron Compact Computers (KCC) AG standard products comply with RoHS legislation.

Since July 1, 2006, there has been a strict adherence to the use of RoHS compliant electronic and mechanical components during the design-in phase of all KCC AG standard products.

1.8.2 RoHS Compliant Production Process

Kontron Compact Computers AG selects external suppliers that are capable of producing RoHS compliant devices. These capabilities are verified by:

- A confirmation from the supplier indicating that their production processes and resulting devices are RoHS compliant.
- If there is any doubt of the RoHS compliancy, the concentration of the previously mentioned substances in a produced device will be measured. These measurements are carried out by an accredited laboratory.

1.8.3 WEEE Application

The WEEE directive is closely related to the RoHS directive and applies to the following devices:

- Large and small household appliances
- IT equipment
- Telecommunications equipment (although infrastructure equipment is exempt in some countries)
- Consumer equipment
- Lighting equipment – including light bulbs
- Electronic and electrical tools
- Toys, leisure and sports equipment
- Automatic dispensers

It does not apply to fixed industrial plants and tools. The compliance is the responsibility of the company that brings the product to market, as defined in the directive. Components and sub-assemblies are not subject to product compliance. In other words, since Kontron Compact Computers AG does not deliver ready-made products to end users the WEEE directive is not applicable for KCC AG. Users are nevertheless encouraged to properly recycle all electronic products that have reached the end of their life cycle.

1.9 Swiss Quality

- 100% Made in Switzerland
- This product was not manufactured by employees earning piecework wages
- This product was manufactured in humane work conditions
- All employees who worked on this product are paid customary Swiss market wages and are insured
- ISO 9000:2001 (quality management system)

1.10 The Swiss Association for Quality and Management Systems

The Swiss Association for Quality and Management Systems (SQS) provides certification and assessment services for all types of industries and services. SQS certificates are accepted worldwide thanks to accreditation by the Swiss Accreditation Service (SAS), active membership in the International Certification Network, IQNet, and co-operation contracts/agreements with accredited partners.

www.sqs.ch

The SQS Certificate ISO 9001:2000 has been issued to Kontron Compact Computers AG in the field of development, manufacturing and sales of embedded computer boards, embedded computer modules and computer systems. The certification is valid for three years at which time an audit is performed for recertification.

2 Introduction

2.1 MSMGE104EX Ordering Information

Part / Option	Part Number	Description
MSMGE104ex	801730	1GE-LAN with PCIe

2.2 Understanding MSMGE104EX Functionality

The MICROSPACE® PCI/104-*Express* expansion card MSMGE has a PCI Express 1 GB LAN controller. The product permits connection to the 1 GB/s Ethernet by means of the full utilization of the PCI Express bus bandwidth (2.5 GB/s).

- 1 GB LAN
- PCI Express bus
- 2x USB
- PCI104: pass-through
- The MSMGE104EX is PCI/104-Express, version 1.0, compliant.

2.3 MICROSPACE® Documentation

This manual is written for the original equipment manufacturer (OEM) who plans to build computer systems based on the single board MICROSPACE-PC. It is for integrators and programmers of systems based on the MICROSPACE-Computer family. This manual provides instructions for installing and configuring the board and describes the system and setup requirements. This document contains information on hardware requirements, interconnections, and details of how to program the system. Please check the Product CD for further information and manuals.

2.4 MSMGE104EX Benefits

MICROSPACE PCI/104-*Express* embedded computer boards comply with the PC/104 Consortium form factor 3.55 × 3.775 inches (90.17 × 95.89 mm) and are available in "stack through" versions.

3 Specifications

Note: All information is subject to change without notice.

3.1 Functional Specifications

Bus

» Standard: PCI/104express

» Size: 32 bit PCIe

3.2 Dimensions

» Length: 90 mm

» Width: 96 mm

» Height: 15 mm

3.3 Electrical Specifications

Power Supply

» Working: 5 Volt / 1W

3.4 Environmental Specifications

Temperature

» Operating, standard version: -25°C to +70°C

» Operating, industrial version: -25°C to +85°C (ask Kontron Compact Computers AG)

» Non-operating (storage): -55°C to +85°C

Vibration

» 5 to 2000 Hz

Humidity

» 5% to 90% (non-condensing)

Shock

» 10 g

3.5 LAN 1000 BASE-T

- » Controller Intel 82573L, from Texas Instruments
- » Enhanced BIOS Yes
- » Memory None
- » Interface RJ45 LAN

4 Detailed System Description

4.1 DOS 1000 Ethernet LAN 82573

Required programs and drivers are located in the directory \DRIVERS\MSMGE104ex\MSDOS.

Create a directory C:\LAN1000 on your hard disk.

Copy the programs and drivers of \DRIVERS\MSMGE104ex\MSDOS onto your HD.

```
CD \LAN1000
LSL
E1000BODI
IPXODI
NETX
CD \
```

Table 82574L.EEP:

```
8888 8888 8887 0B30 F746 0057 FFFF FFFF
FFFF FFFF 026B 0000 8086 109A 8086 80DF
0000 2000 7E54 0000 0014 00DA 0004 2700
6CC9 3150 0732 040B 2984 0000 F000 0706
1008 0000 0F04 7FFF 4D01 FFFF FFFF FFFF
0014 001D 0014 001D AAAF 001E 0000 001D
0100 4000 121C 4007 FFFF FFFF FFFF FFFF
FFFF FFFF FFFF FFFF FFFF FFFF FFFF FFFF
```

Test the transfer with HDTEST using a disk drive on your server!

4.1.1 EEPROM Update

Attention: Do ***not*** use this function, if the EEPROM is ***not*** corrupted.

To download the data in the EEPROM, use the following steps:

- » Copy the folder "x:\ DRIVERS\MSMGE104ex\EEPROM" to a floppy disk.
- » Start MSDOS
- » Type the following string:

  Load data into EEPROM. Load with the command:

  " EEUPDATE –NIC=x –MAC=y –d z "

  x = LAN interface to program

  y = MAC address

  z = EEPROM map file (82573L.EEP for MSMGE104ex)

The EEPROM is now updated again.

4.2 Driver Installation Windows XP

Driver: x:\drivers\MSMGE104ex\XP-W2K

Double click on PRO2KXP.EXE and follow the instructions:



4.2.1 Useful Websites

Working with the MSMGE104ex peripheral board, you might need some information regarding the Intel Boot Agent software. This information can be found on the following websites:

http://support.intel.com

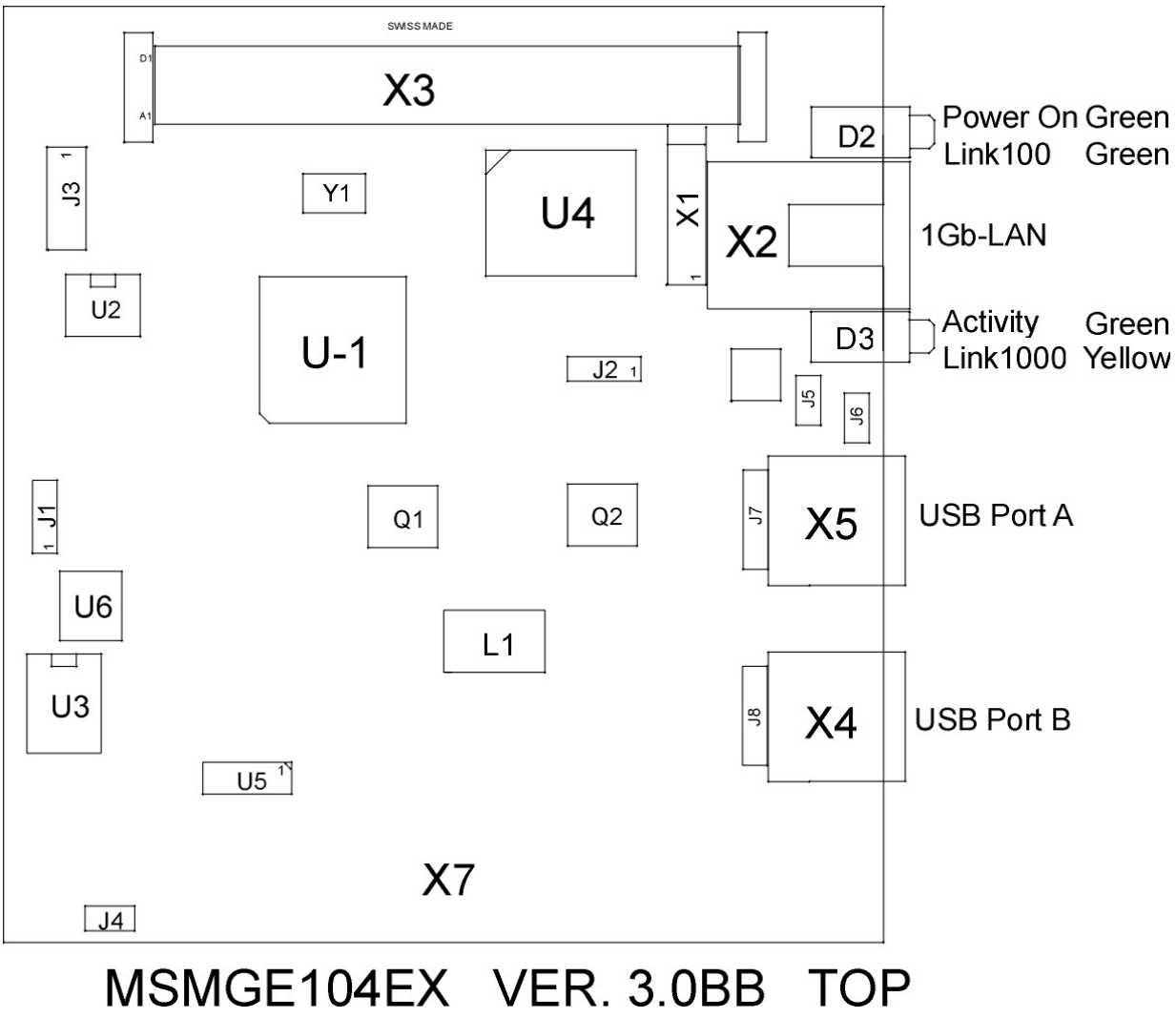
http://www.intel.com/network

http://developer.intel.com

5 MSMGE104EX Connectors & Jumpers

5.1 Connector & Jumper Locations

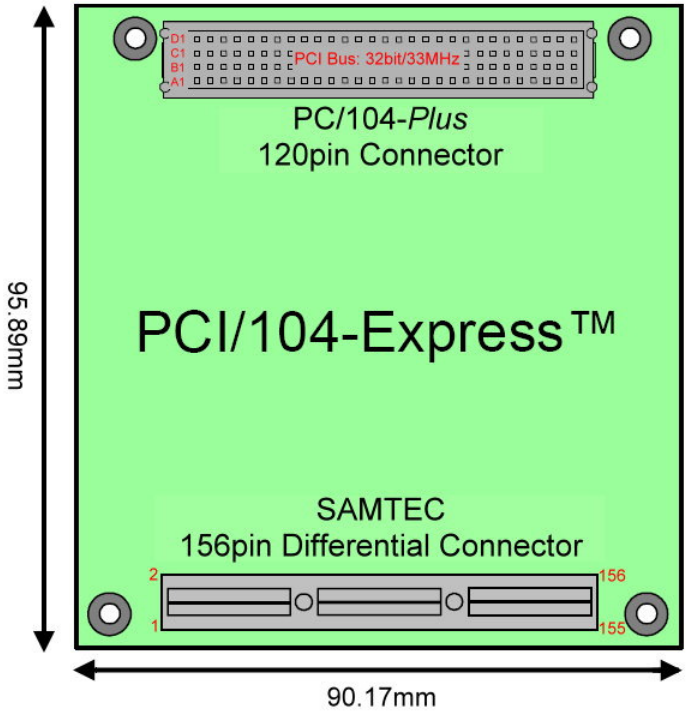
5.1.1 Top View



5.1.2 X6 PCI/104-Express Bus (Bottom View Signal Assignment)

Pin	Signal		Signal	Pin
2	PE_RST#		Reserved (GPIO0)	1
4	3.3V		3.3V	3
6	USB0+		USB1+	5
8	USB0-		USB1-	7
10	GND		GND	9
12	PEx1_0T+		PEx1_1T+	11
14	PEx1_0T-		PEx1_1T-	13
16	GND		GND	15
18	PEx1_3T+		PEx1_2T+	17
20	PEx1_3T-		PEx1_2T-	19
22	GND		GND	21
24	PEx1_0R+	+5 Volts	PEx1_1R+	23
26	PEx1_0R-		PEx1_1R-	25
28	GND		GND	27
30	PEx1_3R+		PEx1_2R+	29
32	PEx1_3R-		PEx1_1R-	31
34	GND		GND	33
36	PEx1_0Clk+		PEx1_1Clk+	35
38	PEx1_0Clk-		PEx1_1Clk-	37
40	5V_Always		5V_Always	39
42	PEx1_3Clkp+		PEx1_2Clk+	41
44	PEx1_3Clk-		PEx1_2Clk-	43
46	PWRGOOD		CPU_DIR	45
48	PEx16_x8_x4_Clk+		SMB_DAT	47
50	PEx16_x8_x4_Clk-		SMB_CLK	49
52	PSON#		SMB_ALERT	51
54	PEG_ENA#		Reserved / Wake#	53
56	GND		GND	55
58	PEx16_0T(0)+ / SDVO0R+		PEx16_0T(8)+	57
60	PEx16_0T(0)- / SDVO0R-		PEx16_0T(8)-	59
62	GND		GND	61
64	PEx16_0T(1)+ / SDVO1G+		PEx16_0T(9)+	63
66	PEx16_0T(1)- / SDVO1G-		PEx16_0T(9)n-	65
68	GND		GND	67
70	PEx16_0T(2)+ / SDVO0B+		PEx16_0T(10)+	69
72	PEx16_0T(2)- / SDVO0B-		PEx16_0T(10)-	71
74	GND		GND	73
76	PEx16_0T(3)+ / SDVO0CLK+		PEx16_0T(11)+	75
78	PEx16_0T(3)- / SDVO0CLK-		PEx16_0T(11)-	77
80	GND		GND	79
82	PEx16_0T(4)+ / SDVO1R+		PEx16_0T(12)+	81
84	PEx16_0T(4)- / SDVO1R-		PEx16_0T(12)-	83
86	GND		GND	85
88	PEx16_0T(5)+ / SDVO1G+		PEx16_0T(13)+	87
90	PEx16_0T(5)- / SDVO1G-		PEx16_0T(13)-	89
92	GND		GND	91
94	PEx16_0T(6)+ / SDVO1B+		PEx16_0T(14)+	93
96	PEx16_0T(6)- / SDVO1B-		PEx16_0T(14)-	95
98	GND		GND	97
100	PEx16_0T(7)+ / SDVO1CLK+		PEx16_0T(15)+	99
102	PEx16_0T(7)- / SDVO1CLK+		PEx16_0T(15)-	101
104	GND		GND	103

Pin	Signal		Signal	Pin
106	SDVO_CLK		SDVO_DAT (PENA#)	105
108	GND		GND	107
110	PEx16_0R(0)+ / SDVO_TVCI+		PEx16_0R(8)+	109
112	PEx16_0R(0)- / SDVO_TVCI-		PEx16_0R(8)-	111
114	GND		GND	113
116	PEx16_0R(1)+ / SDVO_INT+		PEx16_0R(9)+	115
118	PEx16_0R(1)- / SDVO_INT-		PEx16_0R(9)-	117
120	GND		GND	119
122	PEx16_0R(2)+ / SDVO_STALL+		PEx16_0R(10)+	121
124	PEx16_0R(2)- / SDVO_STALL-		PEx16_0R(10)-	123
126	GND		GND	125
128	PEx16_0R(3)+	+12 Volts	PEx16_0R(11)	127
130	PEx16_0R(3)-		PEx16_0R(11)-	129
132	GND		GND	131
134	PEx16_0R(4)+		PEx16_0R(12)+	133
136	PEx16_0R(4)-		PEx16_0R(12)-	135
138	GND		GND	137
140	PEx16_0R(5)+ / SDVO1_INT+		PEx16_0R(13)+	139
142	PEx16_0R(5)- / SDVO1_INT-		PEx16_0R(13)-	141
144	GND		GND	143
146	PEx16_0R(6)+		PEx16_0R(14)p	145
148	PEx16_0R(6)-		PEx16_0R(14)n	147
150	GND		GND	149
152	PEx16_0R(7)+		PEx16_0R(15)+	151
154	PEx16_0R(7)-		PEx16_0R(15)-	153
156	GND		GND	155



5.1.3 X7 PCI/104-Express Bus (Top View Signal Assignment)

Pin	Signal		Signal	Pin
1	Reserved (GPIO0)		PE_RST#	2
3	3.3V		3.3V	4
5	USB1+		USB0+	6
7	USB1-		USB0-	8
9	GND		GND	10
11	PEx1_1T+		PEx1_0T+	12
13	PEx1_1T-		PEx1_0T-	14
15	GND		GND	16
17	PEx1_2T+		PEx1_3T+	18
19	PEx1_2T-		PEx1_3T-	20
21	GND		GND	22
23	PEx1_1R+	+5 Volts	PEx1_0R+	24
25	PEx1_1R-		PEx1_0R-	26
27	GND		GND	28
29	PEx1_2R+		PEx1_3R+	30
31	PEx1_1R-		PEx1_3R-	32
33	GND		GND	34
35	PEx1_1Clk+		PEx1_0Clk+	36
37	PEx1_1Clk-		PEx1_0Clk-	38
39	5V_Always		5V_Always	40
41	PEx1_2Clk+		PEx1_3Clkp+	42
43	PEx1_2Clk-		PEx1_3Clk-	44
45	CPU_DIR		PWRGOOD	46
47	SMB_DAT		PEx16_x8_x4_Clk+	48
49	SMB_CLK		PEx16_x8_x4_Clk-	50
51	SMB_ALERT		PSON#	52
53	Reserved / Wake#		PEG_ENA#	54
55	GND		GND	56
57	PEx16_0T(8)+		PEx16_0T(0)+ / SDVO0R+	58
59	PEx16_0T(8)-		PEx16_0T(0)- / SDVO0R-	60
61	GND		GND	62
63	PEx16_0T(9)+		PEx16_0T(1)+ / SDVO1G+	64
65	PEx16_0T(9)n-		PEx16_0T(1)- / SDVO1G-	66
67	GND		GND	68
69	PEx16_0T(10)+		PEx16_0T(2)+ / SDVO0B+	70
71	PEx16_0T(10)-		PEx16_0T(2)- / SDVO0B-	72
73	GND		GND	74
75	PEx16_0T(11)+		PEx16_0T(3)+ / SDVO0CLK+	76
77	PEx16_0T(11)-		PEx16_0T(3)- / SDVO0CLK-	78
79	GND		GND	80
81	PEx16_0T(12)+		PEx16_0T(4)+ / SDVO1R+	82
83	PEx16_0T(12)-		PEx16_0T(4)- / SDVO1R-	84
85	GND		GND	86
87	PEx16_0T(13)+		PEx16_0T(5)+ / SDVO1G+	88
89	PEx16_0T(13)-		PEx16_0T(5)- / SDVO1G-	90
91	GND		GND	92
93	PEx16_0T(14)+		PEx16_0T(6)+ / SDVO1B+	94
95	PEx16_0T(14)-		PEx16_0T(6)- / SDVO1B-	96
97	GND		GND	98
99	PEx16_0T(15)+		PEx16_0T(7)+ / SDVO1CLK+	100
101	PEx16_0T(15)-		PEx16_0T(7)- / SDVO1CLK+	102
103	GND		GND	104

Pin	Signal		Signal	Pin
105	SDVO_DAT (PENA#)	+12 Volts	SDVO_CLK	106
107	GND		GND	108
109	PEx16_0R(8)+		PEx16_0R(0)+ / SDVO_TVCI+	110
111	PEx16_0R(8)-		PEx16_0R(0)- / SDVO_TVCI-	112
113	GND		GND	114
115	PEx16_0R(9)+		PEx16_0R(1)+ / SDVO_INT+	116
117	PEx16_0R(9)-		PEx16_0R(1)- / SDVO_INT-	118
119	GND		GND	120
121	PEx16_0R(10)+		PEx16_0R(2)+ / SDVO_STALL+	122
123	PEx16_0R(10)-		PEx16_0R(2)- / SDVO_STALL-	124
125	GND		GND	126
127	PEx16_0R(11)		PEx16_0R(3)+	128
129	PEx16_0R(11)-		PEx16_0R(3)-	130
131	GND		GND	132
133	PEx16_0R(12)+		PEx16_0R(4)+	134
135	PEx16_0R(12)-		PEx16_0R(4)-	136
137	GND		GND	138
139	PEx16_0R(13)+		PEx16_0R(5)+ / SDVO1_INT+	140
141	PEx16_0R(13)-		PEx16_0R(5)- / SDVO1_INT-	142
143	GND		GND	144
145	PEx16_0R(14)p		PEx16_0R(6)+	146
147	PEx16_0R(14)n		PEx16_0R(6)-	148
149	GND		GND	150
151	PEx16_0R(15)+		PEx16_0R(7)+	152
153	PEx16_0R(15)-		PEx16_0R(7)-	154
155	GND		GND	156

5.1.4 X2 LAN 1000 Connector

Pin	Signal	Pin	Signal
2	TXN	1	TXP
4	open	3	RXP
6	RXN	5	open
8	open	7	open

5.1.5 X4/X5 USB0/USB1 Connectors

Pin	Signal
1	VCC
2	USB +
3	USB -
4	GND

5.2 Jumpers

The Jumpers for the USB0 and USB1 circuit on this product:

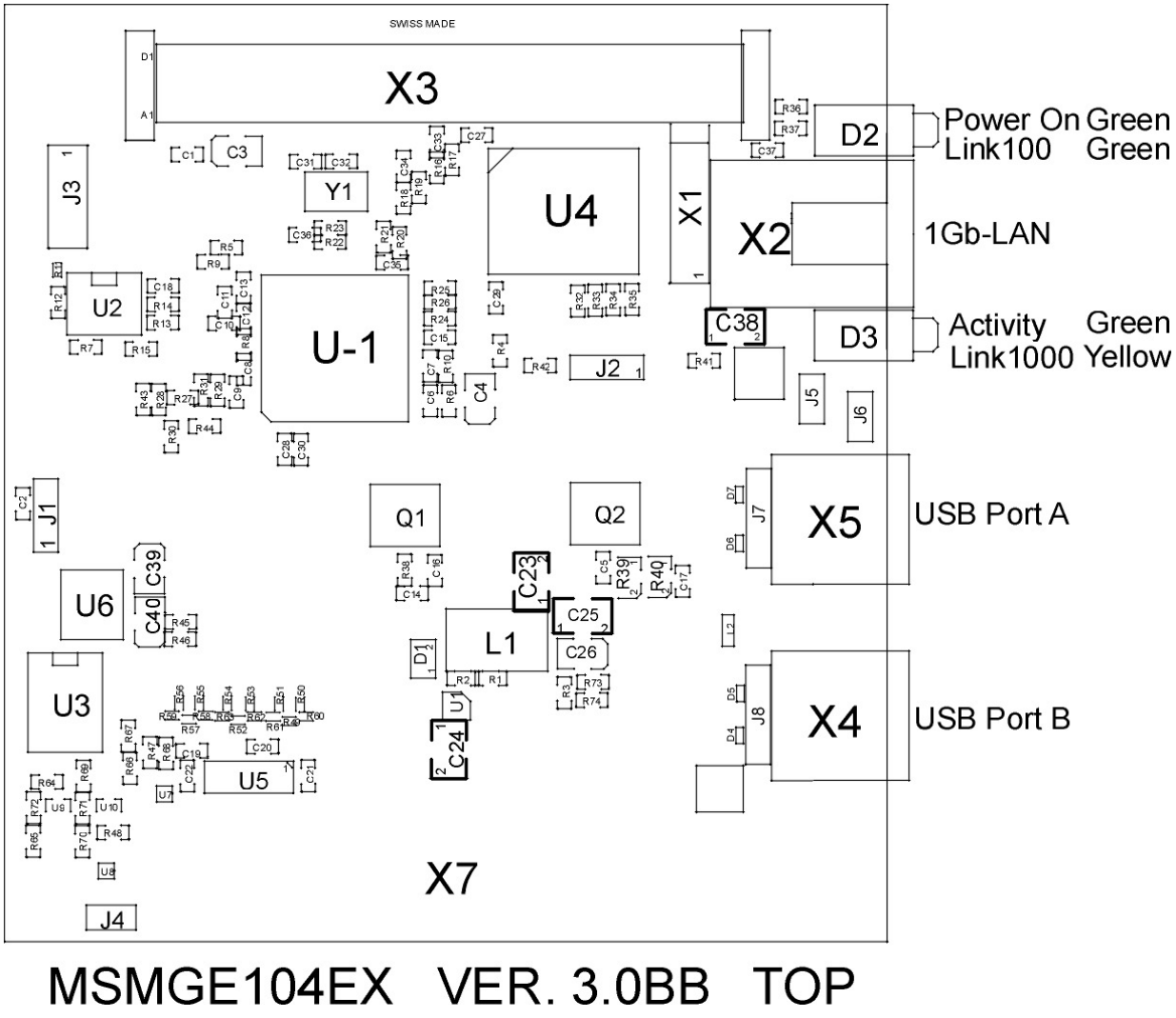
Default settings in bold!

Jumper	Structure	1-2 / Open	2-3 / Closed
J5	USB1	Disable	**Enable**
J6	USB0	Disable	**Enable**

Attention	There are only two USB ports on the PCIe bus. If different PCI/104-*Express* boards with additional USB ports are being used, then the board which will have the USB ports available must be selected. Jumpers **J5** (USB1) and **J6** (USB0) enable or disable the two ports; if they are closed, the USB ports are enabled. Open these jumpers if the two USB ports are already used on another PCI/104-*Express* board. It is also possible to enable one USB port on each board but be careful that USB0 is enabled on one board and USB1 on the other board!

6 Assembly Views

6.1 Top View

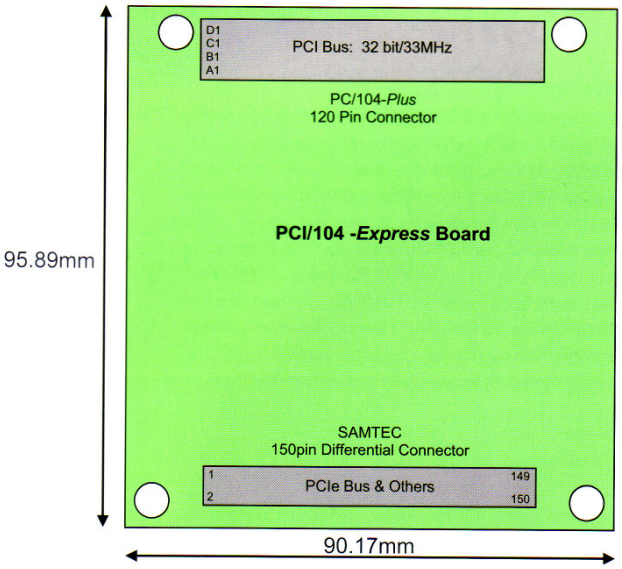


7 Previous Product Versions

7.1 Connectors Version 1.1 and Below

7.1.1 X4 PCI/104-Express Bus (Top View Signal Assignment)

Pin	Signal	Signal	Pin
1	USB_0+	USB_1+	2
3	USB_0-	USB_1-	4
5	PE_RST#	CPU_Direction	6
7	TX_0x1+	TX_1x1+	8
9	TX_0x1-	TX_1x1-	10
11	GND	GND	12
13	TX_3x1+	TX_2x1+	14
15	TX_3x1-	TX_2x1-	16
17	GND	GND	18
19	RX_0x1+	RX_1x1+	20
21	RX_0x1-	RX_1x1-	22
23	GND	GND	24
25	RX_3x1+	RX_2x1+	26
27	RX_3x1-	RX_2x1-	28
29	3.3V_Always	5V_Always	30
31	CK_0x1+	CK_1x1+	32
33	CK_0x1-	CK_1x1-	34
35	3.3V	3.3V	36
37	CK_3x1+	CK_2x1+	38
39	CK_3x1-	CK_2x1-	40
41	3.3V	3.3V	42
43	PEG_CLK+	SMB_DAT	44
45	PEG_CLK-	SMB_CLK	46
47	WAKE#	USB_OC10#	48
49	+12V	+12V	50



Pin	Signal	Signal	Pin
51	SDVO_0_Red+ / PEG_TX0+	SDVO_1_Red+ / PEG_TX4+	52
53	SDVO_0_Red- / PEG_TX0-	SDVO_1_Red- / PEG_TX4-	54
55	GND	GND	56
57	SDVO_0_Green+ / PEG_TX1+	SDVO_1_Green+ / PEG_TX5+	58
59	SDVO_0_Green- / PEG_TX1-	SDVO_1_Green- / PEG_TX5-	60
61	GND	GND	62
63	SDVO_0_Blue+ / PEG_TX2+	SDVO_1_Blue+ / PEG_TX6+	64
65	SDVO_0_Blue- / PEG_TX2-	SDVO_1_Blue- / PEG_TX6-	66
67	GND	GND	68
69	SDVO_0_Clk+ / PEG_TX3+	SDVO_1_Clk+ / PEG_TX7+	70
71	SDVO_0_Clk- / PEG_TX3-	SDVO_1_Clk- / PEG_TX7-	72
73	3.3V	3.3V	74
75	SDVO_TVCLKIN+ / PEG_RX0+	PEG_RX4+	76
77	SDVO_TVCLKIN- / PEG_RX0-	PEG_RX4-	78
79	3.3V	3.3V	80
81	SDVO_0_INT+ / PEG_RX1+	SDVO_1_INT+ / PEG_RX5+	82
83	SDVO_0_INT- / PEG_RX1-	SDVO_1_INT- / PEG_RX5-	84
85	3.3V	3.3V	86
87	SDVO_STALL+ / PEG_RX2+	PEG_RX6+	88
89	SDVO_STALL- / PEG_RX2-	PEG_RX6-	90
91	PEG_Enable#	SMB_ALERT	92
93	PEG_RX3+	PEG_RX7+	94
95	PEG_RX3-	PEG_RX7-	96
97	CaEx_PRST0#	CaEx_PRST1#	98
99	+12V	+12V	100
101	PEG_TX8+	PEG_TX12+	102
103	PEG_TX8-	PEG_TX12-	104
105	+5V	+5V	106
107	PEG_TX9+	PEG_TX13+	108
109	PEG_TX9-	PEG_TX13-	110
111	+5V	+5V	112
113	PEG_TX10+	PEG_TX14+	114
115	PEG_TX10-	PEG_TX14-	116
117	+5V	+5V	118
119	PEG_TX11+	PEG_TX15+	120
121	PEG_TX11-	PEG_TX15-	122
123	+5V	+5V	124
125	PEG_RX8+	PEG_RX12+	126
127	PEG_RX8-	PEG_RX12-	128
129	+5V	+5V	130
131	PEG_RX9+	PEG_RX13+	132
133	PEG_RX9-	PEG_RX13-	134
135	+5V	+5V	136
137	PEG_RX10+	PEG_RX14+	138
139	PEG_RX10-	PEG_RX14-	140
141	SDVO_CLK	SDVO_DAT (PEG_ENA#)	142
143	PEG_RX11+	PEG_RX15+	144
145	PEG_RX11-	PEG_RX15-	146
147	PSON#	PWRGOOD	148
149	+12V	+12V	150

8 Appendix A: Architecture Information

The following sources of information can help you better understand PC architecture.

8.1 Buses

8.1.1 ISA, Standard PS/2 – Connectors

- AT Bus Design: Eight and Sixteen-Bit ISA, E-ISA and EISA Design, Edward Solari, Annabooks, 1990, ISBN 0-929392-08-6
- AT IBM Technical Reference, Volumes 1&2, 1985
- ISA & EISA Theory and Operation, Edward Solari, Annabooks, 1992, ISBN 0929392159
- ISA Bus Specifications and Application Notes, Jan. 30, 1990, Intel
- ISA System Architecture, Third Edition, Tom Shanley and Don Anderson, Addison-Wesley Publishing Company, 1995, ISBN 0-201-40996-8
- Personal Computer Bus Standard P996, Draft D2.00, Jan. 18, 1990, IEEE Inc
- Technical Reference Guide, Extended Industry Standard Architecture Expansion Bus, Compaq 1989

8.1.2 PCI/104

- Embedded PC 104 Consortium
- The consortium provides information about PC/104 and PC/104-Plus technology. You can search for information about the consortium on the Web.
- PCI SIG
- The PCI-SIG provides a forum for its ~900 member companies, who develop PCI products based on the specifications that are created by the PCI-SIG. You can search for information about the SIG on the Web.
- PCI & PCI-X Hardware and Software Architecture & Design, Fifth Edition, Edward Solari and George Willse, Annabooks, 2001, ISBN 0-929392-63-9.
- PCI System Architecture, Tom Shanley and Don Anderson, Addison-Wesley, 2000, ISBN 0-201-30974-2.

8.2 General PC Architecture

- Embedded PCs, Markt&Technik GmbH, ISBN 3-8272-5314-4 (German)
- Hardware Bible, Winn L. Rosch, SAMS, 1997, 0-672-30954-8
- Interfacing to the IBM Personal Computer, Second Edition, Lewis C. Eggebrecht, SAMS, 1990, ISBN 0-672-22722-3
- The Indispensable PC Hardware Book, Hans-Peter Messmer, Addison-Wesley, 1994, ISBN 0-201-62424-9
- The PC Handbook: For Engineers, Programmers, and Other Serious PC Users, Sixth Edition, John P. Choisser and John O. Foster, Annabooks, 1997, ISBN 0-929392-36-1

8.3 Ports

8.3.1 RS-232 Serial

» EIA-232-E standard

» The EIA-232-E standard specifies the interface between (for example) a modem and a computer so that they can exchange data. The computer can then send data to the modem, which then sends the data over a telephone line. The data that the modem receives from the telephone line can then be sent to the computer. You can search for information about the standard on the Web.

» RS-232 Made Easy: Connecting Computers, Printers, Terminals, and Modems, Martin D. Seyer, Prentice Hall, 1991, ISBN 0-13-749854-3

» National Semiconductor: The Interface Data Book includes application notes. Type “232” as search criteria to obtain a list of application notes. You can search for information about the data book on National Semiconductor’s Web site.

8.3.2 Serial ATA

Serial AT Attachment (ATA) Working Group. This X3T10 standard defines an integrated bus interface between disk drives and host processors. It provides a common point of attachment for systems manufacturers and the system. You can search for information about the working group on the Web. We recommend you also search the Web for information on 4.2 I/O cables, if you use hard disks in a DMA3 or PIO4 mode.

8.3.3 USB

USB Specification

USB Implementers Forum, Inc. is a non-profit corporation founded by the group of companies that developed the Universal Serial Bus specification. The USB-IF was formed to provide a support organization and forum for the advancement and adoption of Universal Serial Bus technology. You can search for information about the standard on the Web.

8.4 Programming

» C Programmer’s Guide to Serial Communications, Second Edition, Joe Campbell, SAMS, 1987, ISBN 0-672-22584-0

» Programmer’s Guide to the EGA, VGA, and Super VGA Cards, Third Edition, Richard Ferraro, Addison-Wesley, 1990, ISBN 0-201-57025-4

» The Programmer’s PC Sourcebook, Second Edition, Thom Hogan, Microsoft Press, 1991, ISBN 1-55615-321-X

» Undocumented PC, A Programmer’s Guide to I/O, CPUs, and Fixed Memory Areas, Frank van Gilluwe, Second Edition, Addison-Wesley, 1997, ISBN 0-201-47950-8

9 Appendix B: Document Revision History

Revision	Date	Edited by	Changes
100	05.Feb.2010	WAS	Changed to new Kontron Corporate Design from DLAG V1.1A including title photo.
101	13.Aug.2010	GRP/WAS	Jumper enable/disable corrected.

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