Errata 2

# Manual: 15353

## Manual Index: 01

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# 1. Reference: Page 1 – 6, Chapter 1.5 Features

Figure 1.2 MC68EN360 Channel Assignment:

**The text in the figure:** SMC2 **is changed to read:** SMC1 (SER0)

**The text in the figure:** SMC1 **is changed to read:** SMC2 (TERM)

# 2. Reference: Page 1 – 10, Chapter 1.7 Ordering Information

For the latest product ordering information please contact your nearest Kontron Modular Computers GmbH sales office.

# 3. Reference: Page 2 – 5, Chapter 2.3.3 Boot ROM

Table 2.3 : Supported Boot ROM Chips

**The text in the table:** AM27C4048-120JC

**is changed to read:** AM27C4096-120JC

# 4. Reference: Page 2 – 9, Chapter 2.5.1 VME Master Interface

Table 2.6: External Signal Connection

**The text in the table:** AM Code 17-10

**is changed to read:**

| AM Code (hex) | Function |
|---|---|
| 11 | A24 User Defined 1, Supervisory program access |
| 12 | A24 User Defined 1, Supervisory data access |
| 15 | A24 User Defined 1, non-privileged program access |
| 16 | A24 User Defined 1, non-privileged data access |

**The text in the table:** AM Code 1F-18

**is changed to read:**

| AM Code (hex) | Function |
|---|---|
| 19 | A24 User Defined 2, Supervisory program access |
| 1A | A24 User Defined 2, Supervisory data access |
| 1D | A24 User Defined 2, non-privileged program access |
| 1E | A24 User Defined 2, non-privileged data access |


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# 5. Reference: Page 2 – 8, Chapter 2.5 VMEbus Interface

**The following table is added:**

VMEbus Connector ST1 (P1)

| Pin Nr. | Row A Signal | Row B Signal | Row C Signal |
|---|---|---|---|
| 1 | D00 | BBSY* | D08 |
| 2 | D01 | BCLR* | D09 |
| 3 | D02 | ACFAIL* | D10 |
| 4 | D03 | BG0IN* | D11 |
| 5 | D04 | BG0OUT* | D12 |
| 6 | D05 | BG1IN* | D13 |
| 7 | D06 | BG1OUT* | D14 |
| 8 | D07 | BG2IN* | D15 |
| 9 | GND | BG2OUT* | GND |
| 10 | SYSCLK | BG3IN* | SYSFAIL* |
| 11 | GND | BG3OUT* | BERR* |
| 12 | DS1* | BR0* | SYSRESET* |
| 13 | DS0* | BR1* | LWORD* |
| 14 | WRITE* | BR2* | AM5 |
| 15 | GND | BR3* | A23 |
| 16 | DTACK* | AM0 | A22 |
| 17 | GND | AM1 | A21 |
| 18 | AS* | AM2 | A20 |
| 19 | GND | AM3 | A19 |
| 20 | IACK* | GND | A18 |
| 21 | IACKIN* | SERA | A17 |
| 22 | IACKOUT* | SERB | A16 |
| 23 | AM4 | GND | A15 |
| 24 | A07 | IRQ7* | A14 |
| 25 | A06 | IRQ6* | A13 |
| 26 | A05 | IRQ5* | A12 |
| 27 | A04 | IRQ4* | A11 |
| 28 | A03 | IRQ3* | A10 |
| 29 | A02 | IRQ2* | A09 |
| 30 | A01 | IRQ1* | A08 |
| 31 | -12V | +5VSTDBY | +12V |
| 32 | +5V | +5V | +5V |

* Active signal low


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# 6. Reference: Page 2 – 17, Chapter 2.6.5 Board Control / Status Register

Table: Register Description

**The text in the table for the description of TR-WDG, bit 3:**

**is supplemented as follows:**

Bit 3 must be set to 0 for CXC, and to 1 for ECXC.

# 7. Reference: Page 2 – 20, Chapter 2.8 Serial Communication Ports

Figure 2.2 MC68EN360 Channel Assignment

**The text in the figure:** SMC2 **is changed to read:** SMC1 (SER0)

**The text in the figure:** SMC1 **is changed to read:** SMC2 (TERM)

Table 2.10: Serial Communication Port Configuration

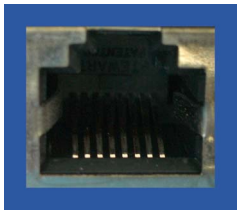
**The text in the table:** SMC2 **is changed to read:** SMC1 (SER0)

**The text in the table:** SMC1 **is changed to read:** SMC2 (TERM)

# 8. Reference: Page 2 – 22, Chapter 2.8.2 SER1, SER2, and SER3 Ports

**The following figure is added before the table referenced: RJ45 Connector**

RJ45 Connector



8 1

**The table referenced:** RJ45 Connector

**is changed to read:** RJ45 Connector – SC-232CCM piggyback


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**The table referenced:**

Mini D-Sub Female Connector

**is deleted.**

**The following table referenced:** RJ45 Connector - SC-Cloop piggyback

| Pin | Signal |
|---|---|
| 1 | NC |
| 2 | NC |
| 3 | GND |
| 4 | TxD |
| 5 | RxD |
| 6 | NC |
| 7 | NC |
| 8 | NC |

**is added.**

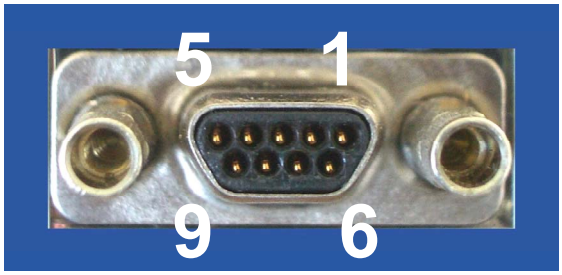
**The following table referenced:** RJ45 Connector - SC-485F/I piggyback

| Pin | Signal |
|---|---|
| 1 | N/C |
| 2 | N/C |
| 3 | GND |
| 4 | +TxD |
| 5 | N/C |
| 6 | N/C |
| 7 | –TxD |
| 8 | N/C |

**is added.**

**The following figure is added:**

Mini D-Sub Male Connector




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**The following table referenced:** Mini D-Sub Male Connector – SC-232CCM piggyback

| Pin | Signal |
|---|---|
| 1 | GND |
| 2 | DTR |
| 3 | TxD |
| 4 | RxD |
| 5 | DCD |
| 6 | N/C |
| 7 | CTS |
| 8 | RTS |
| 9 | DSR |

**is added.**

**The following table referenced**: Mini D-Sub Male Connector – SC-485F/I piggyback

| Pin | Signal |
|---|---|
| 1 | GND |
| 2 | N/C |
| 3 | +TxD |
| 4 | N/C |
| 5 | N/C |
| 6 | N/C |
| 7 | -TxD |
| 8 | N/C |
| 9 | N/C |

**is added.**


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# 9. Reference: Page 2 – 23, Chapter 2.8.3 SER0 and TERM Pinouts

**The table referenced:**

Mini D-Sub Female Connector

**is deleted.**

**The following table referenced**: Mini D-Sub Male Connector

| Pin | Signal |
|---|---|
| 1 | Gnd |
| 2 | N/C |
| 3 | TxD |
| 4 | RxD |
| 5 | N/C |
| 6 | N/C |
| 7 | N/C |
| 8 | N/C |
| 9 | N/C |

**is added.**

# 10. Reference: Page 2 – 28, Note

**The Note is changed to read:**

If the RCLK2 signal (CXM PIN c16) is required, jumper J4 (24 MHz clock) must be opened and the serial drivers delivered by Kontron modified.

# 11. Reference: Page 3 – 4, Chapter 3.2 Jumper Description (Component Side)

Figure 3.1 VM662/VM642 Jumper Layout (Component Side)

**The text in the figure:** J2 ROM Boot **is changed to read:** J2 VME Boot

**The text in the figure:** J1 VME Boot **is changed to read:** J1 ROM Boot

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3.2.1 VME Boot

**The table is changed to read as follows:**

| Jumper | Setting | Description |
|---|---|---|
| J2 | Open | Boot from VMEbus disabled *Default* |
| | Set | Boot from VMEbus enabled |

3.2.2 ROM Boot

**The text in the table:** J2 **is changed to read:** J1

# 12. Reference: Page 4 – 3, Chapter 4.1 VM662/VM642 Address Map

**The description for address area:** $07000000 through $0D000000

**is deleted**

**The Device for address:** $C0000000 **is changed to read:** DRAM

**The Device for address:** $C4000000 **is changed to read:** FLASH

**The Base Address:** $D0000000 **is changed to read:** $D8000000

# 13. Reference: Page CXC – 1, Chapter CXC Address Range

ECXC Address range is added as follows:

$10000000 ECXC CS0
$11000000 ECXC CS1
$12000000 ECXC CS2
$13000000 ECXC CS3
$14000000 ECXC CS4
$15000000 ECXC CS5
$16000000 ECXC CS6
$17000000 ECXC CS7


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# 14. Reference: Appendix Bootstrap Loader

**This appendix is deleted.**

**Please refer to Kontron Manual**:

“Bootstrap Loader, manual ID: 16878, index 0110”

**for information concerning the Bootstrap Loader.**


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