Errata 3

Manual: 15353

Manual Index: 0101

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1. Reference: Page MEM-1, Appendix Memory Piggybacks, Text

The referenced information (text) is revised as follows.

The following text is added:

- DM610 piggyback with 64 MByte SDRAM and 4 MByte FLASH

2. Reference: Page MEM-1, Appendix Memory Piggybacks, Table

The referenced information (table) is revised as follows.

The following table row is added:

Name	Description	Order No.
DM610	Memory piggyback with 64 MByte SDRAM and 4 MByte FLASH	depends on the baseboard*

* The DM610 memory piggyback module can only be ordered with the respective baseboard.

3. Reference: Page MEM-5, Appendix Memory Piggybacks, New Chapter

The referenced information (Appendix) is revised as follows.

The following chapter is added:

5 DM610

The DM610 is a new member within the DM60x family of memory modules supporting various CPU boards. The DM610 provides 64 MByte of SDRAM and 4 MByte of Flash EPROM memory.

To enable or disable write protection, two configurable jumpers, R29 and R30 are present on the board.

Being lead-free/RoHS compliant and being based on SDRAM instead of the obsolete FPM/EDO memory technology, it is intended that the DM610 substitutes all other DM60x modules in the long term.

In contrast to FPM/EDO, the SDRAM technology is based on a synchronous clock. Therefore, the baseboards must provide this clock at the module interface connector in order to be operated with the DM610.

Figure 5-1: DM610 Board Layout (Top View)

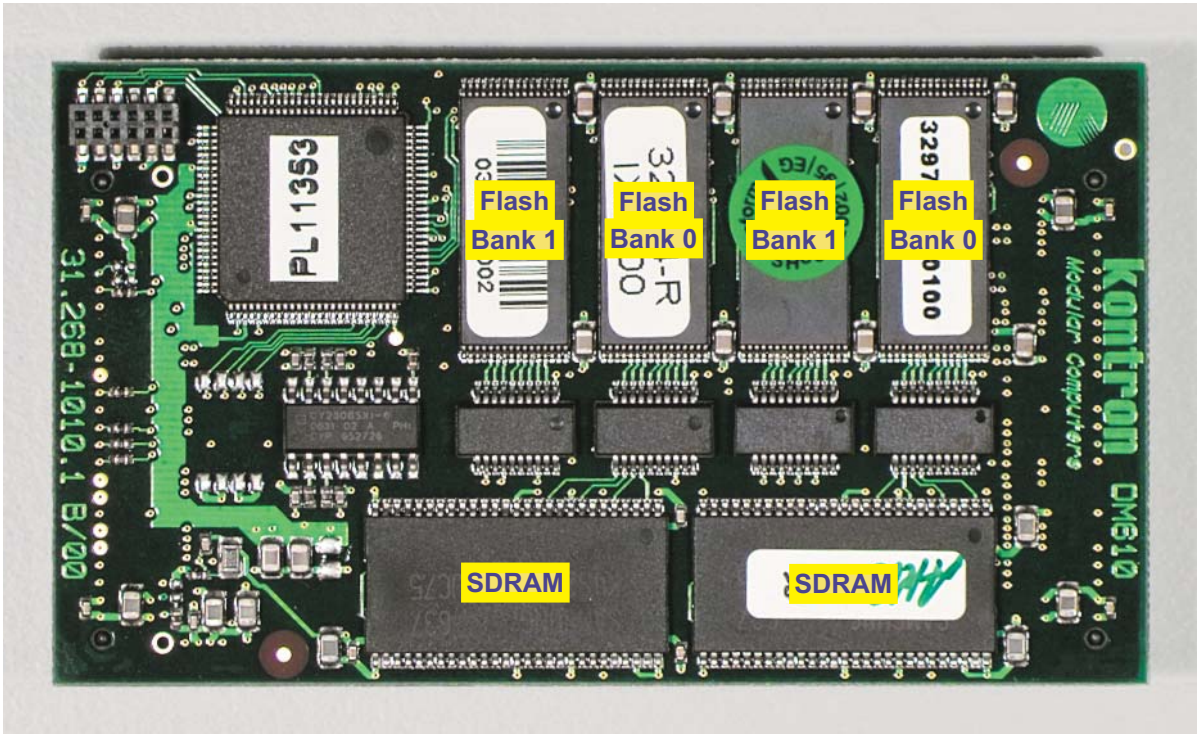
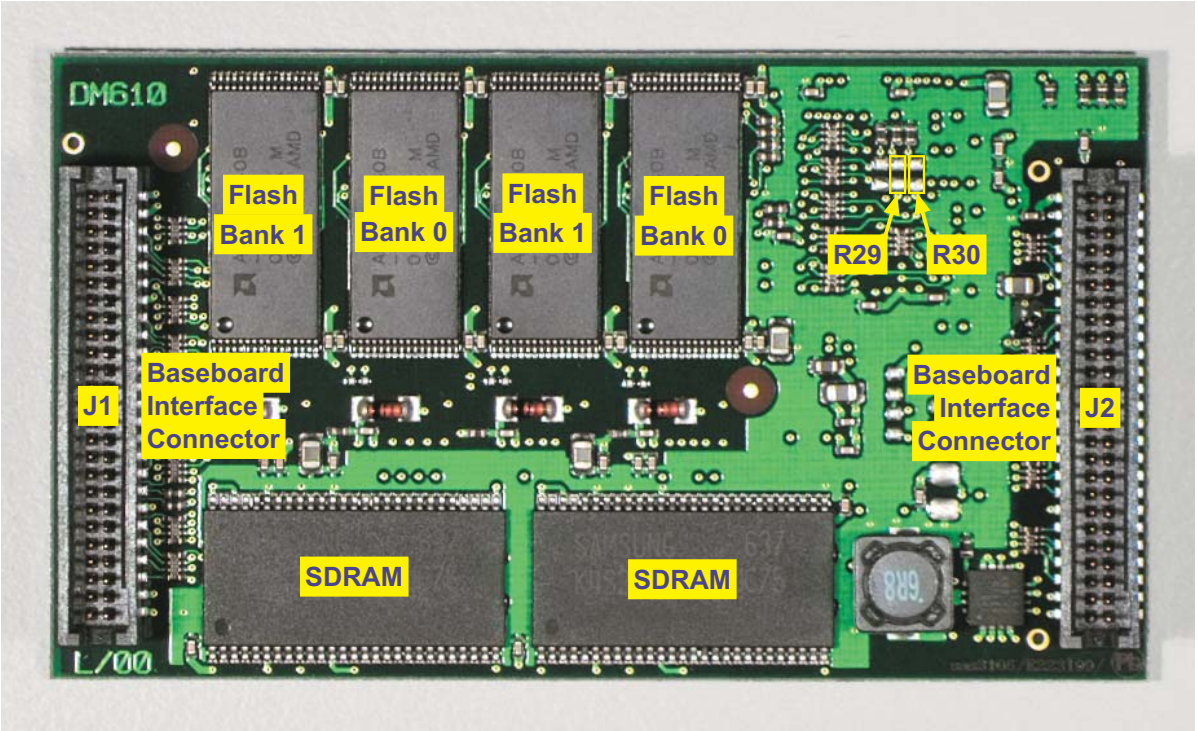


Figure 5-2: DM610 Board Layout (Bottom View)



The following table indicates the jumper settings for the Flash write protection.

Table 5-1: Flash Write Protection

SETTING	DESCRIPTION	4MB FLASH
R29, R30 set	*All Flash EPROM write protected.*	--
R29, R30 open	*No protection. This is the default setting.*	--
R29 set	Flash bank 0 write protected.	Lower 2 MB ($04000000 - $04200000)
R30 set	Flash bank 1 write protected.	Upper 2 MB ($04200000 - $04400000)

The default setting is indicated by using italic bold.

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